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	Filing Date	June 24, 2003
	First Named Inventor	Eric R. Keller
	Examiner Name	Naum B. Levin
	Art Unit	2825
	Patent No.	
Attorney Docket Number		X-1281 US

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X-1281 US
10/603,734



PATENT
Conf. No. 3289

IN THE UNITED STATES PATENT OFFICE

Applicants: Eric R. Keller et al.
Assignee: Xilinx, Inc.
Title: Using Redundant Routing to Reduce Susceptibility to Single Event Upsets in PLD Designs
Serial No.: 10/603,734 File Date: 6/24/2003
Examiner: Naum B. Levin Art Unit: 2825
Docket No.: X-1281 US Conf. No.: 3289

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RESPONSE TO FINAL OFFICE ACTION

In response to the Final Office Action mailed October 1, 2007, Applicants submit the following amendments and remarks.

Amendments to the Specification begin on page 2 of this paper.

Listing of the Claims are reflected in the listing of claims which begins on page 3 of this paper. No amendments are made to the claims in the present Response, but a listing of the claims is included to facilitate prosecution.

Remarks begin on page 11 of this paper.